IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Akira YOSHIDA et al.

Appl. No. 10/061,302

Confirmation No. 2370

Filed: February 4, 2002

For: SIGNAL COMPENSATION

CIRCUIT AND DEMODULATING CIRCUIT WITH HIGH-SPEED AND LOW-SPEED FEEDBACK

LOOPS

Art Unit: 2637

Examiner: Chang, Edith M.

Atty. Docket No. 31869-178004

Customer No. 26694

Submission Of Corrected Drawing

MAIL STOP AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith is one sheet of formal drawings containing corrections to Figure

1. Figure 1, as filed, inadvertently included a "PRIOR ART" label. However, Figure 1 describes an embodiment of the present invention, as indicated, for example, on page 4, lines

18-19. The corrected Figure 1 removes the erroneous prior art label.

Respectfully submitted,

Dated: May 25, 2005

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